

**DISPLAY Elektronik GmbH**

# DATA SHEET

**LCD MODULE**

**DEM 320240T2 VTX-PW-N**

*Product Specification*

*Version:0*

**17.01.2024**

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# GENERAL SPECIFICATION

MODULE NO. :

DEM 320240T2 VTX-PW-N

CUSTOMER

VERSION NO.	CHANGE DESCRIPTION	DATE
0	Original version	17.01.2024

PREPARED BY: LQ

DATE: 17.01.2024

APPROVEDBY: WH

DATE: 17.01.2024

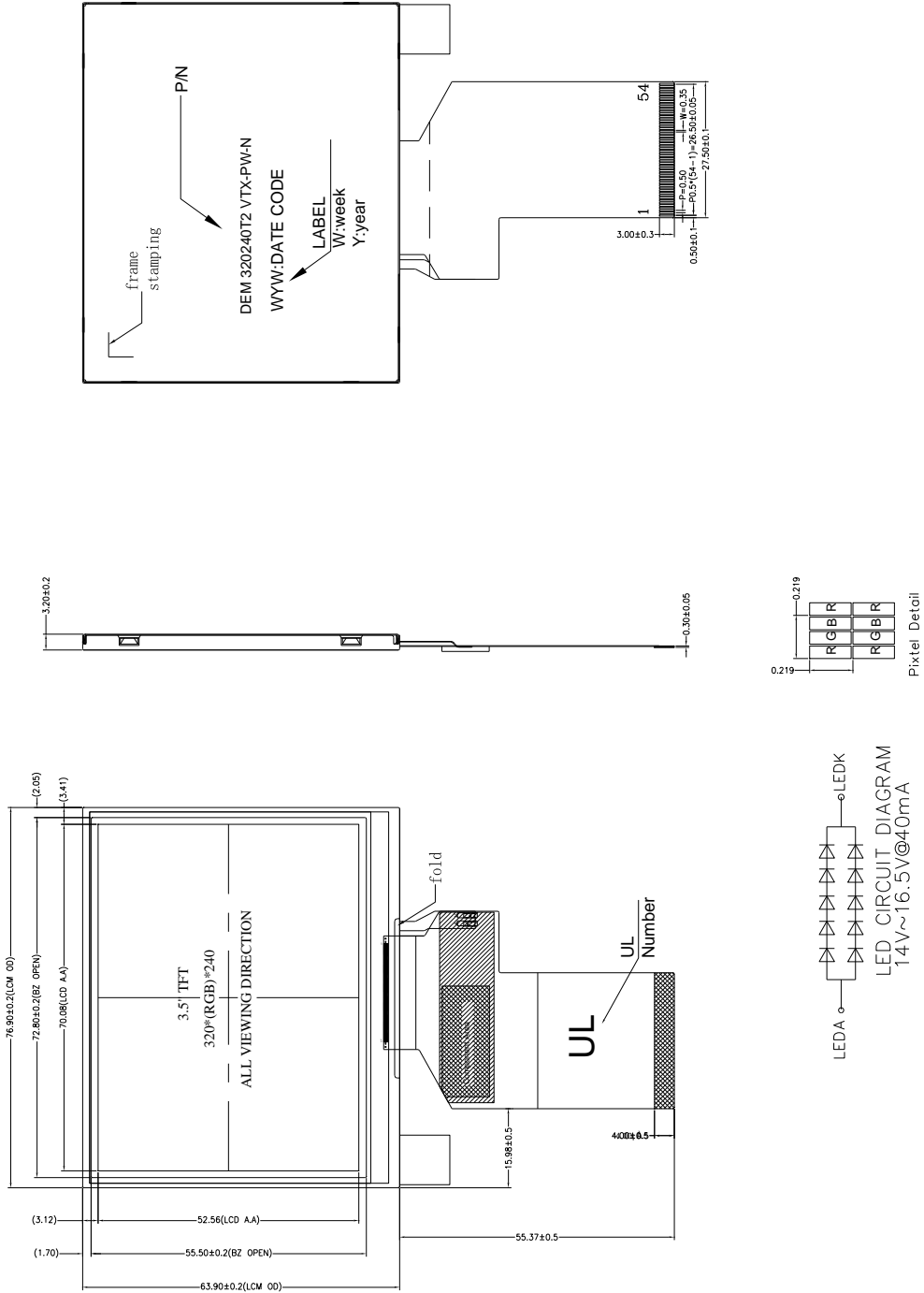
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**1. GENERAL SPECIFICATIONS**

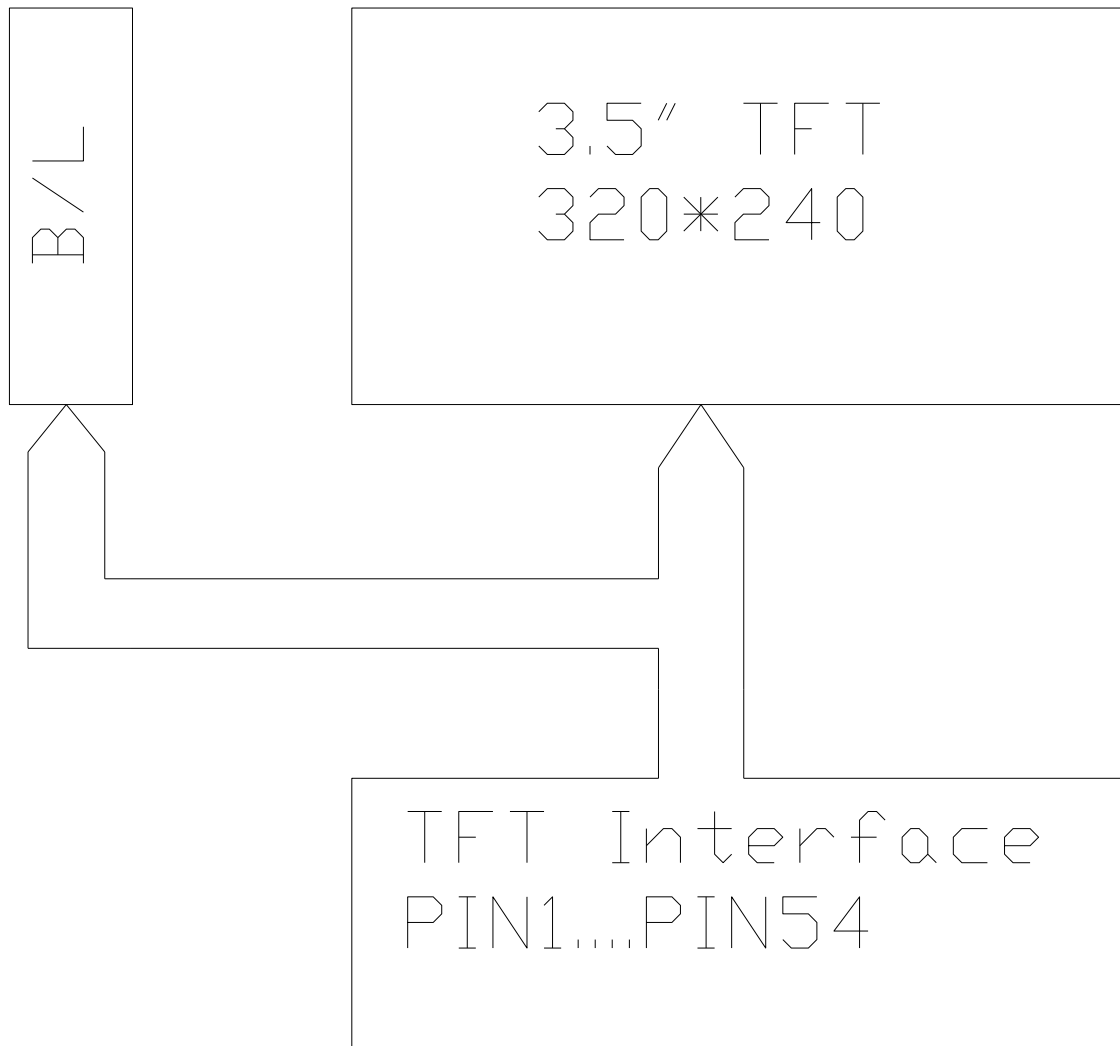
<b>ITEM</b>	<b>STANDARD VALUE</b>	<b>UNIT</b>
LCD SIZE	3.5	inch
LCD TYPE	TFT/IPS/NORMALLY BLACK/ TRANSFLECTIVE /SUNLIGHT READABLE	
MODULE SIZE	76.90 x 63.90 x 3.20	mm
ACTIVE AREA	70.08 x 52.56	mm
PIXEL PITCH	0.219 x 0.219	mm
NUMBER OF PIXELS	320 x 240	
DRIVER IC	ST7272A	
INTERFACE TYPE	24 BIT-RGB + SPI	
TOP POLARIZER TYPE	Anti-glare	
VIEWING DIRECTION	ALL	O'clock
GRAY SCALE INVERSION DIRECTION	-	O'clock
BACKLIGHT TYPE	10-DIES WHITE LED	
TOUCH PANEL	WITHOUT	

2. EXTERNAL DIMENSIONS



Remark:  
 1. Unmarked tolerance is  $\pm 0.3$   
 2. All materials comply with RoHS

**3. BLOCK DIAGRAM**



**4. PIN ASSIGNMENT**

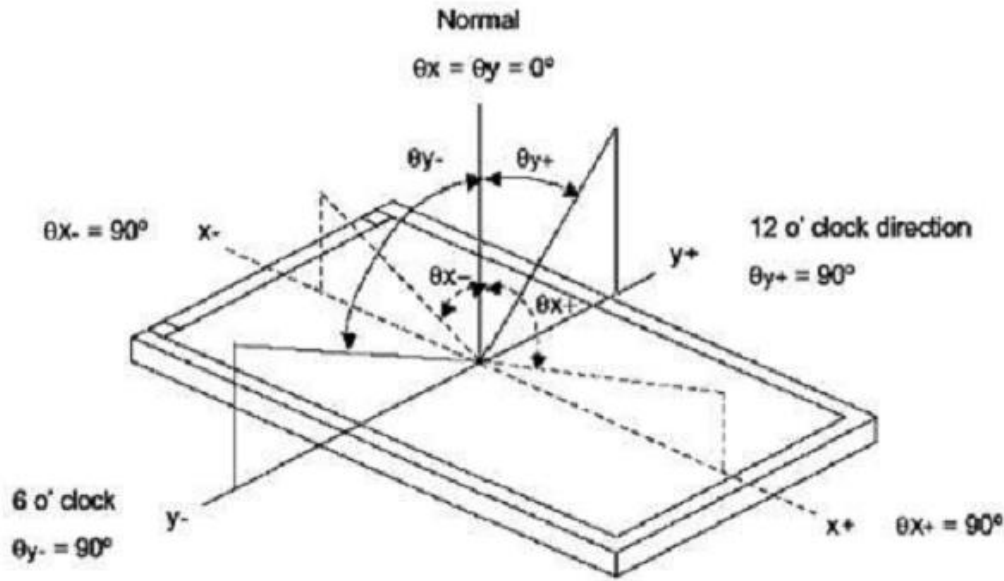
Pin No.	Symbol	Description
1	LEDK	Cathode of LED
2	LEDK	Cathode of LED
3	LEDA	Anode of LED
4	LEDA	Anode of LED
5~7	NC	No connect
8	RESET	Reset pin
9	SPENA	Chip select of serial interface
10	SPCLk	Clock pin of serial interface
11	SPDAT	Data input pin of serial interface
12~19	B0~B7	Blue data bus
20~27	G0~G7	Green data bus
28~35	R0~R7	Red data bus
36	HSYNC	Horizontal sync signal, negative polarity
37	VSYNC	Vertical sync signal, negative polarity
38	DCLK	Clock signal ,latching data at the falling edge
39	NC	No connect
40	NC	No connect
41	VDD	Power supply
42	VDD	Power supply
43~51	NC	No connect
52	DEN	Display enable pin from controller
53	GND	Power ground
54	GND	Power ground

**5. OPTICAL CHARACTERISTICS**

ITEM	SYMBOL	CONDITIONS	SPECIFICATIONS			UNIT	NOTE
			MIN	TYP.	MAX		
Luminance	L	$I_L=40\text{mA}$		1000		Cd/m <sup>2</sup>	
Contrast ratio	CR	$\theta =0^\circ$	640	800			
Response time	T <sub>ON</sub>	25°C		30	40	ms	
	T <sub>OFF</sub>			30	40		
CIE COLOUR COORDINATE	RED	RX	VIEWING NORMAL ANGLE	0.60	0.62	0.64	
		RY		0.34	0.36	0.38	
	GREEN	GX		0.35	0.37	0.39	
		GY		0.57	0.59	0.61	
	BLUE	BX		0.12	0.14	0.16	
		BY		0.08	0.10	0.12	
	WHITE	WX		0.31	0.33	0.35	
		WY		0.35	0.37	0.39	
VIEWING ANGLE	Hor.	$\theta_{x+}$	CR $\geq$ 10	70	80	Degree	
		$\theta_{x-}$		70	80		
	Ver.	$\theta_{y+}$		70	80		
		$\theta_{y-}$		70	80		
Uniformity	Un		80			%	



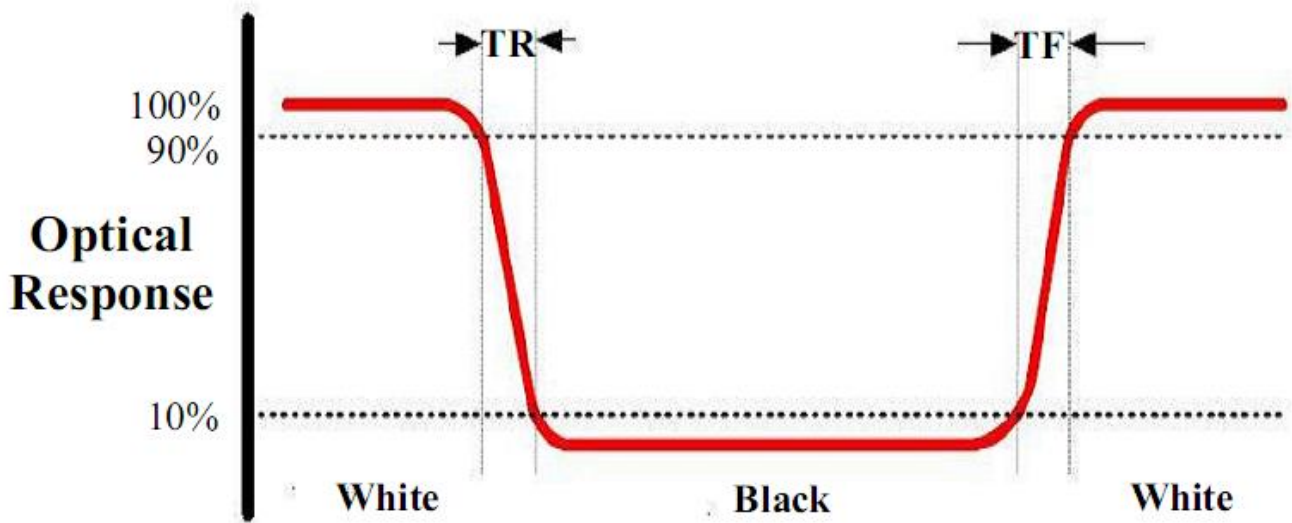
**Note 1: Definition of Viewing Angle  $\theta_x$  and  $\theta_y$ :**



**Note 2: Definition of contrast ratio CR:**

$$CR = \frac{\text{Luminance of white state}}{\text{Luminance of black state}}$$

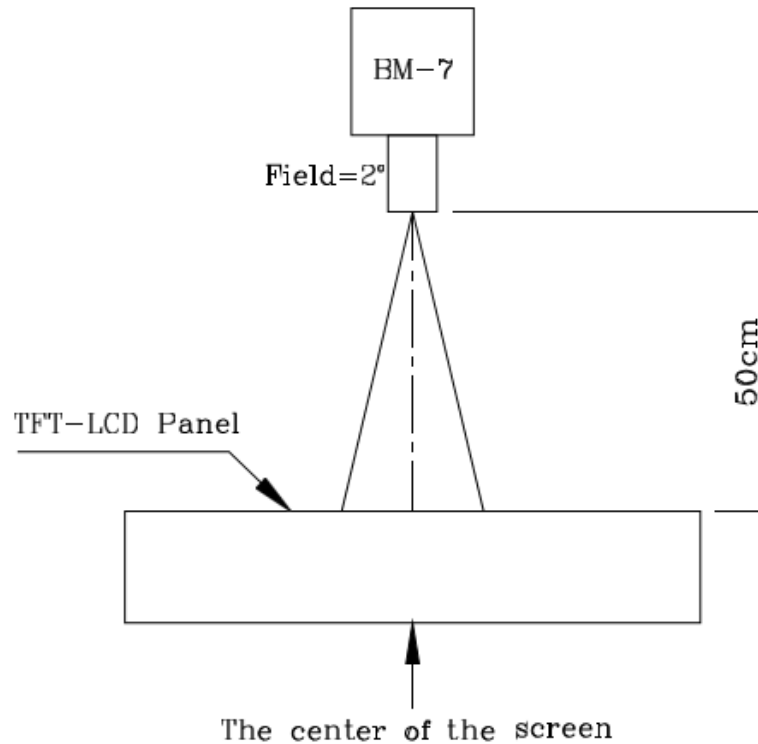
**Note 3: Definition of Response Time ( $T_r, T_f$ )**



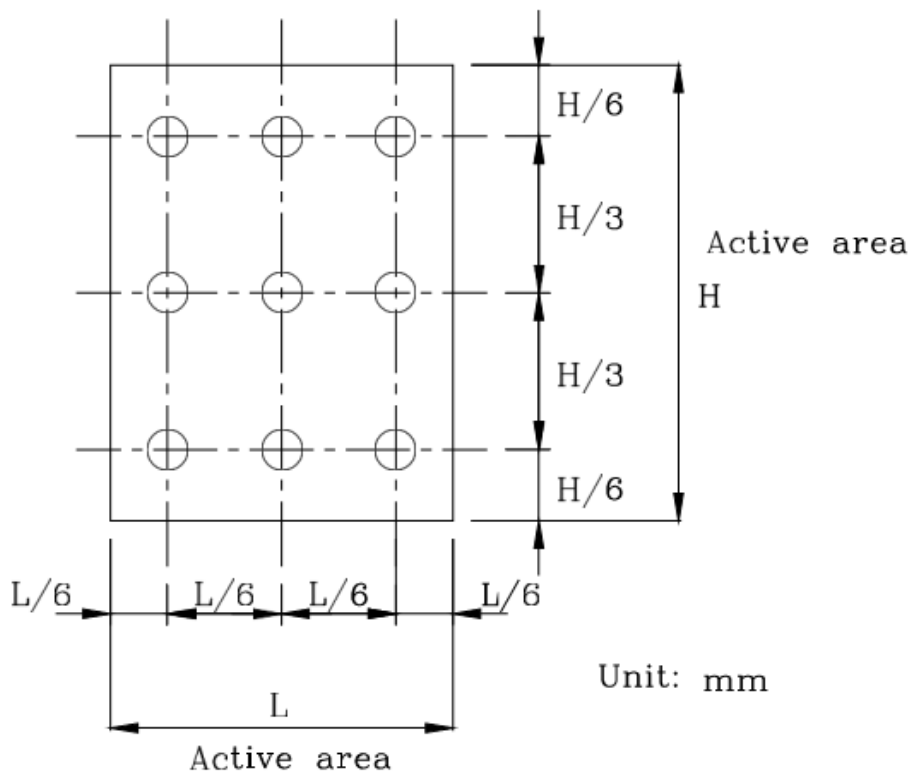
**Note 4: Definition of Luminance**

**① The Brightness Test Equipment Setup**

Field=2° (As measuring “black” image, field=2° is the best testing condition)



**② The Brightness Test Point Setup**



**6. ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	MIN	MAX	UNIT
Power Supply Voltage	VDD	-0.3	4.5	V
Supply voltage for logic	VDD	-0.3	4.5	V
Supply current (One LED)	I <sub>LED</sub>		30	mA
Operating temperature	Top	-30	+85	°C
Storage temperature	Tst	-30	+85	°C

**7. ELECTRICAL CHARACTERISTICS****7.1 Input power**

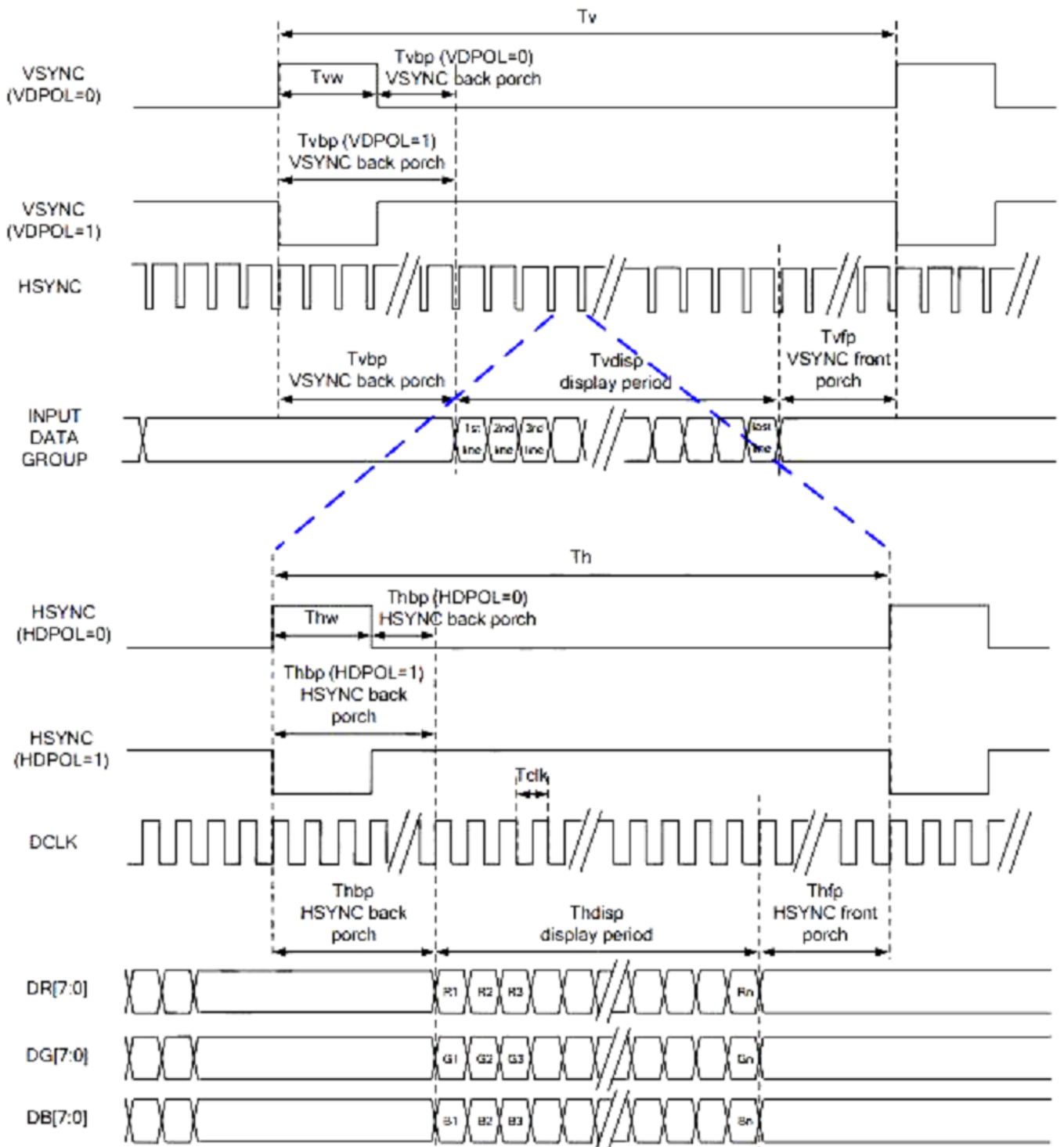
ITEM	SYMBOL	Min	TYP	MAX	UNIT
Supply voltage for analog	VDD	3.0	3.3	3.6	V
Supply voltage for logic	VDD	3.0	3.3	3.6	V
Input voltage	V <sub>IL</sub>	GND	-	0.3VDD	
	V <sub>IH</sub>	0.7VDD	-	VDD	
Input leakage current	I <sub>LKG</sub>	-1		1	μA

**7.2 Backlight Driving Conditions**

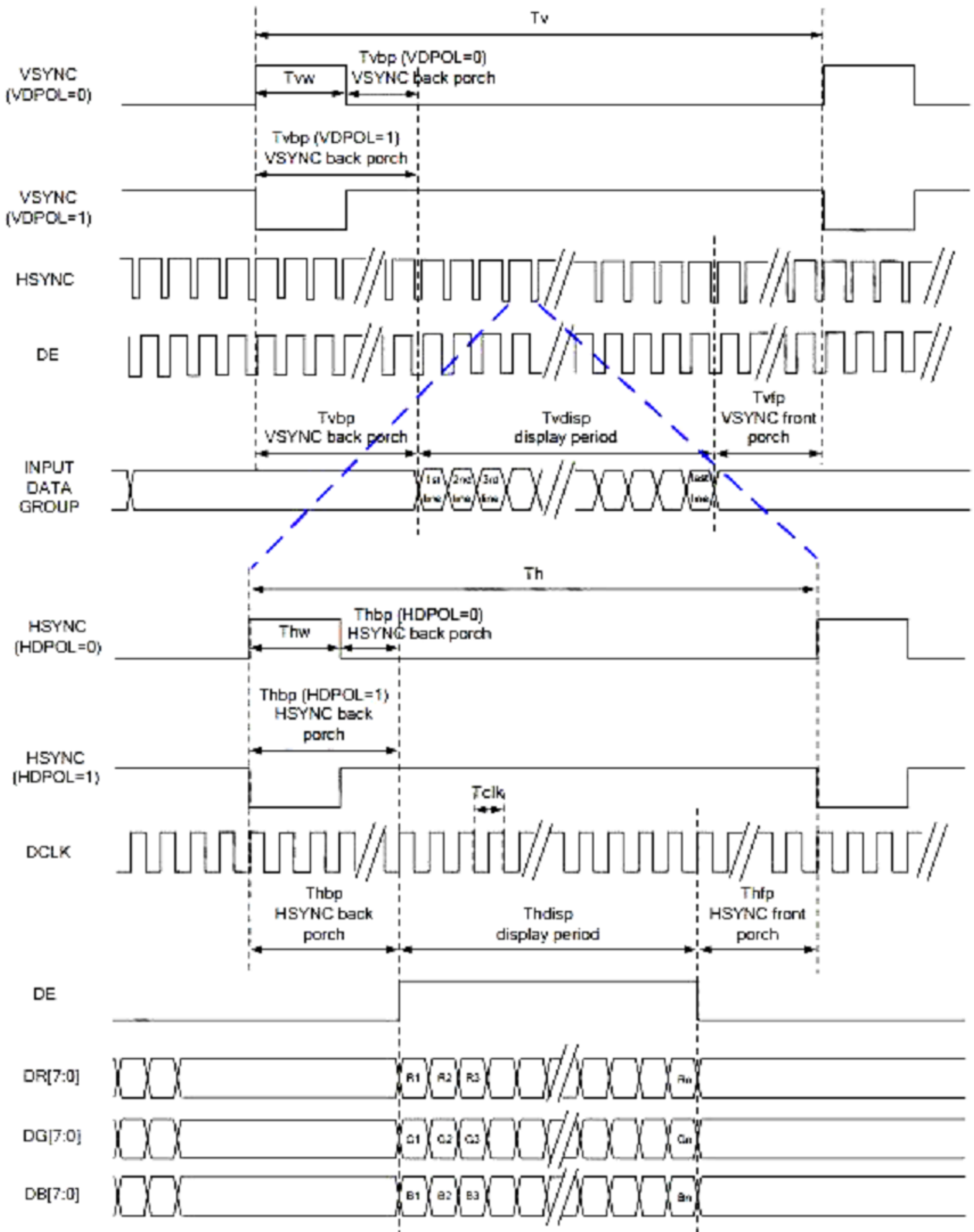
ITEM	SYMBOL	SPECIFICATIONS			UNIT	Remark
		MIN	TYP.	MAX		
Voltage for LED backlight	V <sub>f</sub>	14		16.5	V	I <sub>L</sub> =40mA
Current for LED backlight	I <sub>L</sub>		40		mA	
Power consumption	P	0.56		0.66	W	
LED life time		30000	50000		H <sub>r</sub>	

7.3 TIMING CHARACTERISTICS

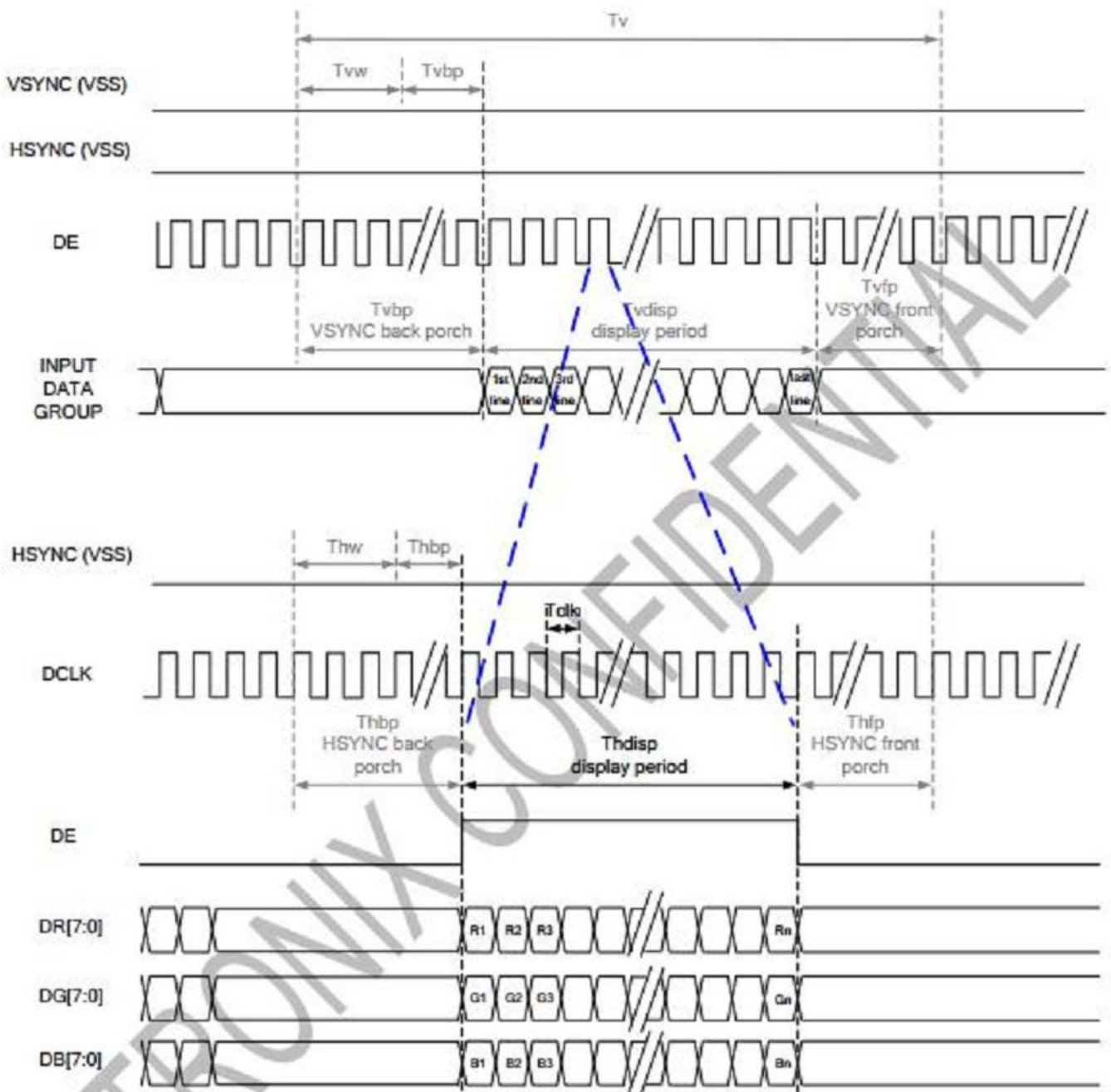
7.3.1 SYNC MODE



7.3.2 SYNC DE-MODE



7.3.3 DE MODE



RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

Note: "Input" means these signals are driven by host side.

7.3.4 Parallel 24 bit RGB Timing Table

Parallel 24-bit RGB Input Timing (PVDD=VDD=VDDI= 3.3V, AGND= 0V, TA=25°C)

Parallel 24-bit RGB Input Timing Table							
Item	Symbol	Min.	Typ.	Max.	Unit	Remark	
DCLK Frequency	Fclk	5	6	8	MHz		
DCLK Period	Tclk	125	167	200	ns		
HSYNC	Period Time	Th	325	371	438	DCLK	
	Display Period	Thdisp		320		DCLK	
	Back Porch	Thbp	3	43	43	DCLK	By H_BLANKING setting
	Front Porch	Thfp	2	8	75	DCLK	
	Pulse Width	Thw	2	4	43	DCLK	
VSYNC	Period Time	Tv	244	260	289	HSYNC	
	Display Period	Tvdisp		240		HSYNC	
	Back Porch	Tvbp	2	12	12	HSYNC	By V_BLANKING setting
	Front Porch	Tvfp	2	8	37	HSYNC	
	Pulse Width	Tvw	2	4	12	HSYNC	

Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.

**8. RELIABILITY TEXT****8.1 Standard Specification for Reliability of LCD Module**

<b>NO.</b>	<b>TEST ITEM</b>	<b>CONDITIONS</b>
1	HIGH TEMPERATURE STORAGE	TA=+85°C      240H
2	LOW TEMPERATURE STORAGE	TA=-30°C      240H
3	HIGH TEMPERATURE OPERATION	TA=+85°C      240H
4	LOW TEMPERATURE OPERATION	TA=-30°C      240H
5	MOISTURE STORAGE	+60°C, 90%RH,      MAX OF 240H
6	THERMAL SHOCK STORAGE	-30°C for 30 minutes → normal temperature for 5 minutes → +85 °C for 30 minutes → normal temperature for 5 minutes.as one cycle
7	PACKING VIBRATION	Frequency range:10Hz ~55Hz Amplitude of vibration: 1.5mm Sweep time: 12,min,X,Y,Z 2hours for each direction
8	ESD	Air: ±8KV 150Pf/300 Ω 5 times
		Contact: ±4KV 150Pf/300 Ω 5 times



**9. LCD MODULES HANDLING PRECAUTIONS**

- The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- If the display panel is damaged and the liquid crystal substance inside it leaks out, do not get any in your mouth. If the substance come into contact with your skin or clothes promptly wash it off using soap and water.
- Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarize carefully.
- To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - Be sure to ground the body when handling the LCD module.
  - Tools required for assembly, such as soldering irons, must be properly grounded.
  - To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
  - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- Storage precautions  
When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags designed to prevent static electricity charging under low temperature / normal humidity conditions (avoid high temperature / high humidity and low temperatures below 0°C).Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

**10. OTHERS**

- Liquid crystals solidify at low temperature (below the storage temperature range) leading to defective orientation of liquid crystal or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to a strong shock at a low temperature.
- If the LCD modules have been operating for a long time showing the same display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. Abnormal operating status can be resumed to be normal condition by suspending use for some time. It should be noted that this phenomena does not adversely affect performance reliability.
- To minimize the performance degradation of the LCD modules resulting from caused by static electricity, etc. exercise care to avoid holding the following sections when handling the modules:
  - Exposed area of the printed circuit board
  - Terminal electrode sections.